

*C<sup>2</sup>*

4. (Twice Amended) A semiconductor device comprising a semiconductor substrate, and a plurality of aluminum conductors formed adjacent to one another on a portion of a main face of the substrate which aluminum conductors comprise aluminum as a main constituent thereof, a film adjacent to said aluminum conductors which adjacent film comprises titanium or titanium nitride as a main constituent thereof, said aluminum conductors containing copper and nickel.

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10. (Twice Amended) A semiconductor device comprising a semiconductor substrate, and aluminum conductors formed on a portion of a main face of the substrate which aluminum conductors comprise aluminum as a main constituent thereof, said aluminum conductors having at least one area in which conductor spacing is not more than 0.4  $\mu\text{m}$ , a film adjacent to said aluminum conductors which adjacent film comprises one kind selected from the group consisting of ruthenium, platinum and iridium as main constituent thereof, said aluminum conductors containing copper, wherein said aluminum conductors contain nickel not less than 0.02 at.% but not more than 1 at.%.

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12. (Amended) A semiconductor device comprising a semiconductor substrate, and aluminum conductors formed on a portion of a main face of the substrate which aluminum conductors comprise aluminum as a main constituent thereof, said aluminum conductors containing copper and nickel, wherein each of the aluminum conductors has at least one area in which conductor spacing is not more than 0.4  $\mu\text{m}$  and wherein the content of nickel contained in said aluminum conductors is not less than 0.2 at.% but not more than 1 at.%

13. (Amended) A semiconductor device comprising:  
a semiconductor substrate,  
a plurality of aluminum conductors formed adjacent to one another on a portion of a main face of the substrate which aluminum conductors comprise aluminum as a main constituent thereof, said aluminum conductors containing copper; and  
means for suppressing diffusion of copper atoms in said aluminum conductors to prevent precipitation of the copper from one of said aluminum conductors to an adjacent one of said aluminum conductors.

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Please add new claims 15-32 as follows:

15. A semiconductor device comprising a semiconductor substrate, and a plurality of aluminum conductors formed adjacent to one another on a portion of a main face of the substrate which aluminum conductors comprise aluminum as a main constituent thereof, said aluminum conductors containing copper and also containing nickel in an amount sufficient to prevent precipitation of said copper between adjacent ones of said aluminum conductors.

16. A semiconductor device according to claim 1, further comprising an insulating film overlying said aluminum conductors on said portion of a main face of the substrate.

17. A semiconductor device according to claim 4, further comprising an insulating film overlying said aluminum conductors on said portion of a main face of the substrate.

18. A semiconductor device according to claim 10, further comprising an insulating film overlying said aluminum conductors on said portion of a main face of the substrate.

19. A semiconductor device according to claim 13, further comprising an insulating film overlying said aluminum conductors on said portion of a main face of the substrate.

20. A semiconductor device according to claim 15, further comprising an insulating film overlying said aluminum conductors on said portion of a main face of the substrate.

21. A semiconductor device according to claim 1, wherein an interconnect pattern of the aluminum conductors is formed by being etched, and the nickel in the aluminum conductors is of a sufficient amount to prevent a precipitation of the copper in the aluminum conductors so that precipitate containing copper is prevented from remaining during the etching to prevent a short in the interconnect pattern caused by the precipitate containing the copper.

22. A semiconductor device according to claim 4, wherein an interconnect pattern of the aluminum conductors is formed by being etched, and the nickel in the aluminum conductors is of a sufficient amount to prevent a precipitation of the copper in the aluminum conductors so that precipitate containing copper is prevented from remaining during the etching to prevent a short in the interconnect pattern caused by the precipitate containing the copper.

23. A semiconductor device according to claim 13, wherein an interconnect pattern of the aluminum conductors is formed by being etched, and the means for suppressing diffusion of copper atoms comprises nickel being provided in the aluminum conductors in a sufficient amount to prevent the precipitation of the copper in the aluminum conductors so that precipitate containing copper is prevented from remaining during the etching to prevent a short in the interconnect pattern caused by the precipitate containing the copper.

24. A semiconductor device according to claim 1, wherein one of the aluminum conductors is electrically connected to the semiconductor substrate through a plug.

25. A semiconductor device according to claim 4, wherein said aluminum conductors have at least one area in which conductor spacing is not more than 0.4  $\mu\text{m}$ .

26. A semiconductor device according to claim 10, wherein said aluminum conductors have at least one area in which conductor spacing is not more than 0.4  $\mu\text{m}$ .

27. A semiconductor device according to claim 12, wherein said aluminum conductors have at least one area in which conductor spacing is not more than 0.4  $\mu\text{m}$ .

28. A semiconductor device according to claim 13, wherein said aluminum conductors have at least one area in which conductor spacing is not more than 0.4  $\mu\text{m}$ .

29. A semiconductor device according to claim 15, wherein said aluminum conductors have at least one area in which conductor spacing is not more than 0.4  $\mu\text{m}$ .

30. A semiconductor device according to claim 21, wherein said aluminum conductors have at least one area in which conductor spacing is not more than 0.4  $\mu\text{m}$ .

31. A semiconductor device according to claim 22, wherein said aluminum conductors have at least one area in which conductor spacing is not more than 0.4  $\mu\text{m}$ .

32. A semiconductor device according to claim 23, wherein said aluminum conductors have at least one area in which conductor spacing is not more than 0.4  $\mu\text{m}$ .